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(54) **SCALABLE PHASED-ARRAY SYSTEM FOR WIRELESS SYSTEMS**

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- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 282 days.

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 CPC **H04L 5/1461** (2013.01); **H01Q 3/30** (2013.01); **H04B 1/401** (2013.01)

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 CPC H04L 5/1461; H04L 5/1469; H04L 29/06; H01Q 3/30; H04B 1/401; H04B 1/50; H04B 1/56; H04W 28/04; H04W 88/06
 See application file for complete search history.

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(57) **ABSTRACT**

A scalable phased-array system for a wireless system includes: a plurality of transceivers, which are switched to form a transmitter mode and a receiver mode by means of time division duplexing (TDD), wherein each transceiver includes: a millimeter wave (mmWave) up-conversion circuit used to convert a baseband (BB) transmitter signal into an intermediate frequency (IF) transmitter signal; a power divider/combiner circuit used to divide the IF transmitter signal into a plurality of pairs of IF transmitter differential signals in the transmitter mode, and combine a plurality of pairs of BB receiver differential signals into a BB receiver signal in the receiver mode; and a beamforming circuit used to convert the pairs of the IF transmitter differential signals into a plurality of radio frequency (RF) transmitter signals in the transmitter mode, and convert a plurality of RF receiver signals into the pairs of the BB receiver differential signals in the receiver mode.

10 Claims, 5 Drawing Sheets

